

## Brief Profile

**Name:** Raj Kumar

**Date of Birth:** 9<sup>th</sup> November 1987

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### **Educational Qualification:**

**Ph.D. (Electronics & Communication Engineering):** Motilal Nehru National Institute of Technology Allahabad, Prayagraj [2015-2021].

**M.Tech (Digital Systems):** Motilal Nehru National Institute of Technology Allahabad, Prayagraj [2013-15].

**B.Tech (Electronics & Communication Engineering):** Dr. K.N. Modi Institute of Engineering & Technology, Modinagar, Ghaziabad, AKTU [2007-11].

### **Experience:**

- Working as an Assistant Professor at Meerut Institute of Engineering and Technology, Meerut (UP) in Electronics and Communication Engineering Department since 11<sup>th</sup> Sept. 2021.
- Worked as TAs (Teaching Assistant) in Laboratories-Electronic Circuit Design, Electronic Devices and Circuits, Digital Electronics Lab, Electronics Engineering Lab, Microprocessor Lab and Microelectronics and VLSI Lab from July 2016 to June 2021 in MNNIT Allahabad.

### **Area/Subjects of Interest:**

Electronics Devices and Circuits, Digital Electronics, Analog Electronics, Microprocessor, Mathematics, VLSI Design (Verilog)

### **STC/FDP/Summer/Winter Schools/Workshops/Seminars attended:**

- Attended one week online faculty development program on “Artificial Intelligence and Machine Learning Using Python” during 10<sup>th</sup> August 2020 to 15<sup>th</sup> August 2020 at IIIT, Noida.
- Presented two papers on “6<sup>th</sup> IEEE Uttar Pradesh Section International Conference on Electrical, Electronics and Computer Engineering (UPCON-2019)” during 8-10 November, 2019 at AMU Aligarh.
- Attended “2<sup>nd</sup> International Conference on VLSI, Communication and Signal Processing (VCAS 2019)” during 21-23 October, 2019 at MNNIT Allahabad, Prayagraj.
- Attended one week short term course on “Advances in Wireless and Optical Networks” during May 27<sup>th</sup>-June 1<sup>st</sup>, 2019 at MNNIT Allahabad, Prayagraj.
- Attended one week short term course on “Advanced Embedded Systems & Microelectronics (AESM 2019)” during 1<sup>st</sup> April 2019 to 5<sup>th</sup> April 2019 at MNNIT Allahabad, Prayagraj.
- Six weeks summer training on “Embedded System” during 14/06/2010 to 29/07/2010 at DUCAT Noida.
- Six weeks summer training on “Programmable Logic Control System” during 22/06/2009 to 01/08/2009 at Hindalco Industries Ltd, Sonbhadra.

### **Publications:**

### ***International Journals:***

1. Raj Kumar, Ritesh Kumar Jaiswal and Ram Awadh Mishra, "Perspective and Opportunities of Modulo  $2^n-1$  Multipliers in Residue Number System: A Review," *Journal of Circuits, Systems and Computers*, vol.29, no.11, p. 2030008, 2020 (SCI Indexed).
2. Ritesh Kumar Jaiswal, Raj. Kumar, and Ram Awadh Mishra, "Area Efficient Memoryless Reverse Converter for New Four Moduli Set  $\{2^{n-1}, 2^n - 1, 2^n + 1, 2^{2n+1} - 1\}$ ," *Journal of Circuits, Systems and Computers*, vol.27, no.5, p. 1850075, 2018 (SCI Indexed).
3. Raj Kumar and Ram Awadh Mishra, "Design and analysis of RNS-based sign detector for moduli set  $\{2^n- 1, 2^n, 2^n + 1\}$ ," *Indonesian Journal of Electrical Engineering and Computer Science*, vol.22, no.1, pp. 62-70, 2021 (Scopus Indexed).

### ***International Conferences:***

1. Raj Kumar and Ram Awadh Mishra, "Design of Efficient Sign Detector for Moduli Set  $\{2^n- 1, 2^n, 2^n + 1\}$  in Residue Number System," In *2019 International Conference on Electrical, Electronics and Computer Engineering (UPCON)* (pp. 1-5). *IEEE*, 2019.
2. Raj Kumar, Ritesh Kumar Jaiswal and Ram Awadh Mishra, "Efficient Design for High Speed and Low Area Reverse Converter for Moduli Set  $\{2^n, 2^{2n}-1, 2^{2n}+1\}$ ," In *2019 International Conference on Electrical, Electronics and Computer Engineering (UPCON)* (pp. 1-6). *IEEE*, 2019.

### ***Book:***

1. P. Karuppanan, Vishnu Shankar, Raj Kumar, "Basic Truncated and Multi-Modulus Multiplier" Lambert Academic Publishing (August 2015).