

Brief Profile

Name: Dr. Deepak Kumar Agrawal

Date of Birth: 06th February 1984

Educational Qualification:

- Ph.D. completed from Aligarh Muslim University (AMU) Aligarh (U.P.), India, in 2020.
- M.Tech completed from Aligarh Muslim University (AMU) Aligarh (U.P.), India, (CPI = 8.44) in 2014.
- B.Tech in Electronics & Communication Engineering from Hindustan College of Science & Technology, Mathura (U.P.), India (Aggregate of 69.86 %) in 2009.

Work Experience:

- *Teaching: 04 years*
- *Research / Industry: Five*

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Area/Subjects of Interest: Current mode analog circuit design, VLSI design

Teaching:

Subjects Taught at UG level: Digital design, Analog integrated circuits, Digital signal processing

Research Publications

- Journals: 9

STC/FDP/Summer/Winter Schools/Workshops/Seminars attended:

- Attended workshop on **Research Evaluation Workshop (MEITY)** held at AU college of Engineering, Visakhapatnam, India during 16-18 November, 2017.
- Attended workshop on **Circuit and System Design Challenges for IoT** held at AMU Aligarh, India on February 18, 2017.
- Attended workshop on **Analog IC Design using SCL CMOS Technology** held at AMU Aligarh, India on October 9, 2016.
- Attended workshop on **Research Paper Writing & Publication Ethics** held at GLA University, Mathura, India on September 24, 2016.
- Attended workshop on **IC Design for Industry: Analog, Mixed-Signal and Memory Chips** held at AMU Aligarh, India during 20-21 February 2016.
- Attended workshop on **Emerging RF Technologies: A Design Perspective** held at AMU Aligarh, India on February 13, 2016.
- Attended workshop on **Recent Trends in Embedded System Design** held at AMU Aligarh, India on March 2, 2013.

LIST OF PUBLICATIONS

INTERNATIONAL JOURNALS

1. **D. Agrawal**, and S. Maheshwari “Design and implementation of current mode circuit for digital modulation”, *Integration*, Vol. 78, pp. 118-123, 2021.
2. **D. Agrawal**, and S. Maheshwari, “High-Performance Electronically Tunable Analog Filter Using a Single EX-CCCII”, *Circuits, Systems, and Signal Processing*, vol. 40, pp.1127-1151, 2020.
3. **D. Agrawal**, and S. Maheshwari, “Electronically tunable grounded immittance simulators using an EX-CCCII”, *International Journal of Electronics*, Vol. 107(10), pp. 1625–1648, 2020
4. **D. Agrawal**, and S. Maheshwari, “An active-C current-mode universal first-order filter and oscillator”, *Journal of Circuits, Systems and Computers*, 28(13), pp.1950219, 2019
5. **D. Agrawal**, and S. Maheshwari, “Cascadable current mode instrumentation amplifier”, *AEU-International Journal of Electronics and Communications*, Vol. 94, pp.91-101, 2018
6. **D. Agrawal**, and S. Maheshwari, “[Current mode filters with reduced complexity using a single EX-CCCII](#)” *AEU-International Journal of Electronics and Communications*, Vol. 80, pp. 86-93, 2017.
7. **D. Agrawal**, and S. Maheshwari, “Current-mode precision full-wave rectifier circuits”, *Circuits, Systems, and Signal Processing*, Vol. 36(11), pp. 4293-4308, 2017
8. S. Maheshwari, and **D. Agrawal**, “Cascadable and tunable analog building blocks using EX-CCCII”, *Journal of Circuits, Systems and Computers*, Vol. 26(06), pp.1750093, 2017.
9. S. Maheshwari, and **D. Agrawal** , “[High performance voltage-mode tunable all-pass section](#)” *Journal of Circuits, Systems and Computers*, Vol. 24(06), pp. 1550080, 2015

INTERNATIONAL CONFERENCES

1. **D. Agrawal**, and S. Maheshwari, “Electronically Tunable Floating Inductance Simulator Using a Single EX-CCCII”, 7th International Conference on Signal Processing and Integrated Networks (SPIN), **DOI:** [10.1109/SPIN48934.2020.9070939](https://doi.org/10.1109/SPIN48934.2020.9070939), pp. 130-134, 2020.
2. S. Maheshwari, and **D. Agrawal**, “Universal Biquadratic Current-Mode Filter Using a Single EX-CCCII”, International Conference on Electrical, Electronics and Computer Engineering (UPCON-2019), **DOI:** [10.1109/UPCON47278.2019.8980155](https://doi.org/10.1109/UPCON47278.2019.8980155), pp. 1-5, 2019.
3. **D. Agrawal**, and S. Maheshwari, Low Voltage Current Mode Instrumentation Amplifier. International Conference on Computing, Communication, and Intelligent Systems (ICCCIS), **DOI:** [10.1109/ICCCIS48478.2019.8974467](https://doi.org/10.1109/ICCCIS48478.2019.8974467), pp. 20-23, 2019
4. **D. Agrawal**, and S. Maheshwari, “Electronically Tunable Multiphase Sinusoidal Oscillator Using EX-CCCIs”, 15th IEEE India Council International Conference (INDICON), **DOI:** [10.1109/INDICON45594.2018.8987178](https://doi.org/10.1109/INDICON45594.2018.8987178), pp. 1-4, 2018
5. **D. Agrawal**, and S. Maheshwari, “Electronically Tunable Current Mode Universal

Filter using a Single MXCCCII”, 2nd IEEE International Conference on Power Electronics, Intelligent Control and Energy Systems (ICPEICES), DOI: [10.1109/ICPEICES.2018.8897406](https://doi.org/10.1109/ICPEICES.2018.8897406), pp. 1093-1096, 2018.

6. **D. Agrawal**, and S. Maheshwari, “Electronically tunable configurable analog block using a single EX-CCCII”, International Conference on Multimedia, Signal Processing and Communication Technologies (IMPACT-2017), DOI: [10.1109/MSPCT.2017.8364010](https://doi.org/10.1109/MSPCT.2017.8364010), pp. 227-231, 2017.