# Brief Profile

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| **Name:** | Aditi Singh |
| **Date of Birth:** | 08/01/1990 |
| **Educational Qualification:** | |
| *Ph. D.* | - |
| *M. Tech* | AKTU, Lucknow (ECE) |
| *B. Tech* | GBTU, Lucknow (ECE) |
| **Work Experience:** | |
| • *Teaching* | 2.5 years |
| • *Research/Industry* | - |
| **E-mail ID:** | aditi.singh@miet.ac.in |
| **Contact No:** | (M): +91-9719863164 |
| **Area/Subjects of Interest** | Signals & Systems, Control Systems, Principle of Communication, Electronics Engg. |
| **Teaching:** | |
| *Subjects Taught at UG level* | Basic Electronics, Signals & Systems, Control Systems |
| *Subjects Taught at PG level* | Discrete Time Signal Processing |
| **Research Guidance** | |
| *No. of Ph.D./M.Tech Guided* | - |
| **Research Publications** | |
| • Journals | 01 |
| • Conferences | |
| • Book Chapters | - |
| **Patent/IPR** (Books Published etc.) | - |
| **No. of National/International Conferences attended/ Paper Presented** | 02 |
| **STC/FDP/Summer/Winter Schools/Workshops/Seminars attended** | 01 |
| **Memberships of the Professional Societies** | - |
| **Awards/Honors** | Many times winner in Debate at district level, |
| **Any other relevant Information** | Gate Qualified |

# LIST OF PUBLICATIONS

**Journal Papers**

1. Aditi Singh and Pravesh Singh, "*Design and Analysis of Stepped Impedance Low Pass Filter using Microstrip Line*, KIET International Journal of Communications & Electronics (KIET-IJCE), Vol. 3, Issue No. 2, December 2015, ISSN: 2320 – 8996.